

# Patent Assignment Abstract of Title

**Total Assignments: 1****Application #:** 10688250**Filing Dt:** 10/16/2003**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** US20040222450**Pub Dt:** 11/11/2004**Inventor:** David Tsang**Title:** MRAM architecture with a bit line located underneath the magnetic tunneling junction device**Assignment: 1****Reel/Frame:** 014631/0950 **Received:** 10/30/2003 **Recorded:** 10/16/2003 **Mailed:** 05/20/2004 **Pages:** 2**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignor:** TSANG, DAVID**Exec Dt:** 10/11/2003**Assignee:** APPLIED SPINTRONICS TECHNOLOGY, INC.

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